

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Rule 1.53(b) Divisional Application of:

ANEZAKI, Toru et al.

Group Art Unit: 2813 (prior)

Serial No.: Div. of 10/091,289

Examiner: Nguyen, T. (prior)

Filed: July 25, 2003

For: SEMICONDUCTOR DEVICE AND MANUFACTURING METHOD THEREOF

INFORMATION DISCLOSURE STATEMENT
PURSUANT TO 37 CFR 1.97(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

July 25, 2003

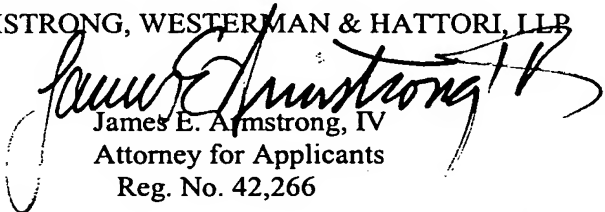
Sir:

This Information Disclosure Statement is being filed in order to comply with Applicant's duty of disclosure under 37 CFR 1.56. The documents listed on the Form PTO-1449 were made of record in parent application Serial No. 09/696,945.

The Commissioner is authorized to charge our Deposit Account No. 01-2340 for any fee which is deemed by the Patent and Trademark Office to be required to effect consideration of this statement.

Respectfully submitted,

ARMSTRONG, WESTERMAN & HATTORI, LLP


James E. Armstrong, IV
Attorney for Applicants
Reg. No. 42,266

JAM/plb
Atty. Docket No. 001443B
Suite 1000
1725 K Street, N.W.
Washington, D.C. 20006
(202) 659-2930



23850

PATENT TRADEMARK OFFICE

Enclosure: PTO-1449

INFORMATION DISCLOSURE CITATION PTO-1449	Atty. Docket No. 001443B	Serial No. Div. of 10/091,289
	Applicant(s): ANEZAKI, Toru et al.	
	Filing Date: July 25, 2003	Group Art Unit: 2813

U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Name	Date	Class	Subclass	Filing Date (If appropriate)
_____	AA	6,015,734	Huang et al.	1-2000		
_____	AB	5,738,961	Chen, Jeng-Horng	4-1998		
_____	AC	5,889,335	Kuroi et al.	3-1999		
_____	AD	6,040,242	Kakehashi	3-2000		
_____	AE	6,063,548	Chu et al.	5-2000		
_____	AF	6,107,132	Wang et al.	8-2000		
_____	AG	6,159,820	Park	12-2000		
_____	AH	5,460,996	Ryou	10-1995		
_____	AI	6,087,216	Wang	7-2000		
_____	AJ	6,143,603	Ding	11-2000		
_____	AK	5,389,560	Park	2-1995		
_____	AL	5,780,338	Jeng et al.	7-1998		
_____	AM	6,054,394	Wang	4-2000		
_____	AN	6,239,022	Seo et al.	5-2001		
_____	AO	6,164,850	Speakman	12-2000		
_____	AP	6,174,782	Lee	1-2001		

OTHER DOCUMENTS

_____	AQ	Wolf "Silicon process for the VLSI Era" 1986, vol. 1, pages 191-194		
_____	AR			
<table border="1" style="width: 100%;"> <tr> <td style="width: 50%;">Examiner</td> <td style="width: 50%;">Date Considered</td> </tr> </table>			Examiner	Date Considered
Examiner	Date Considered			